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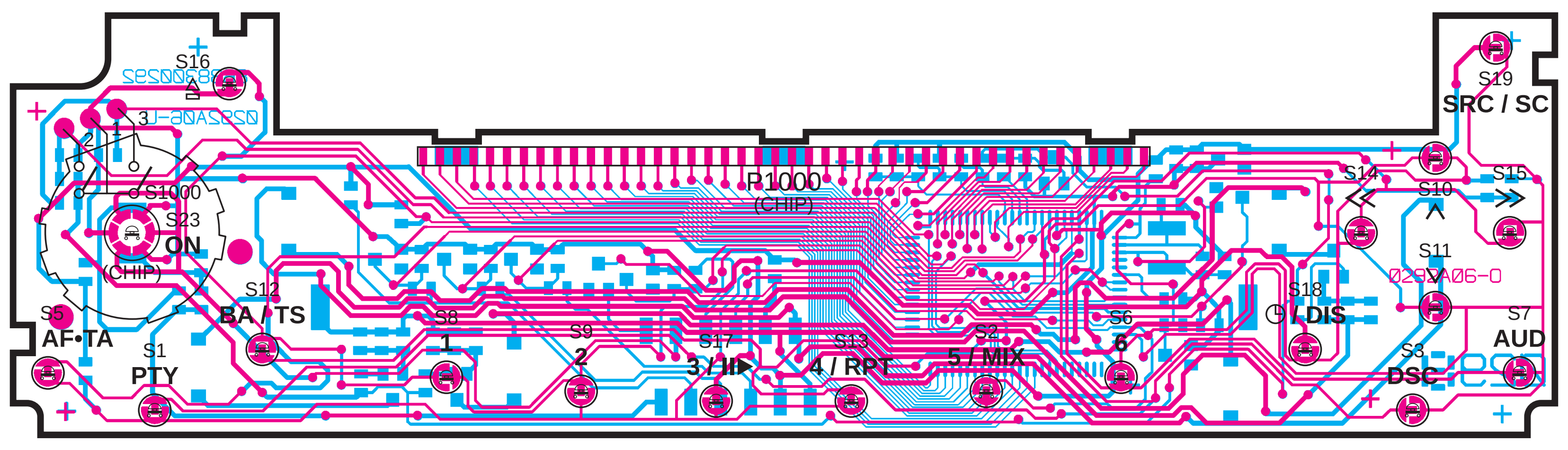
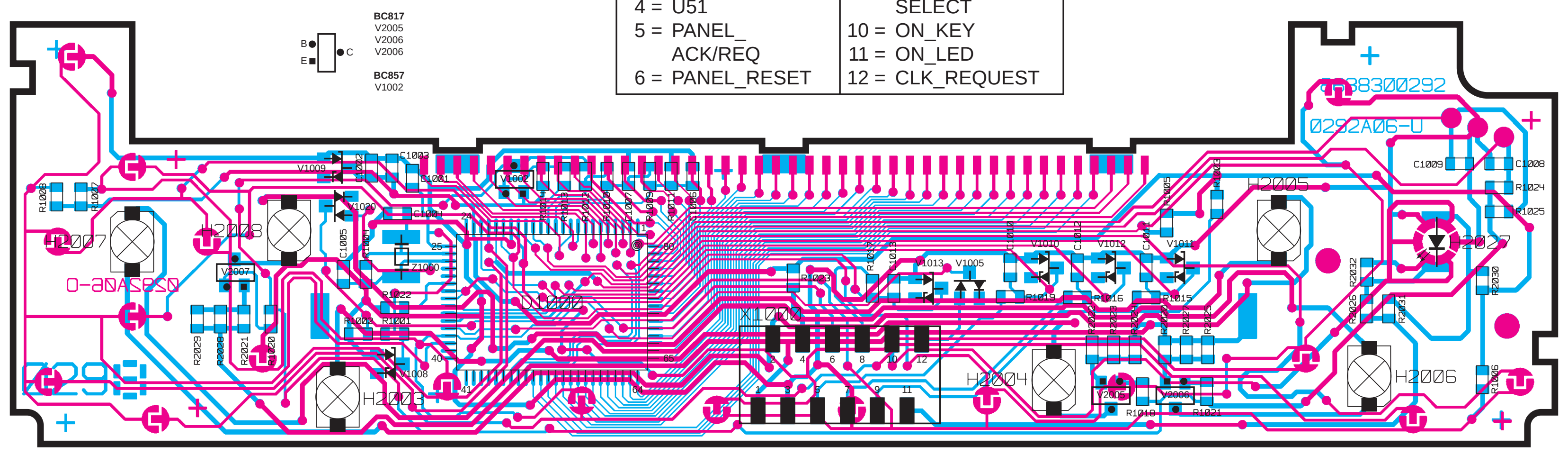
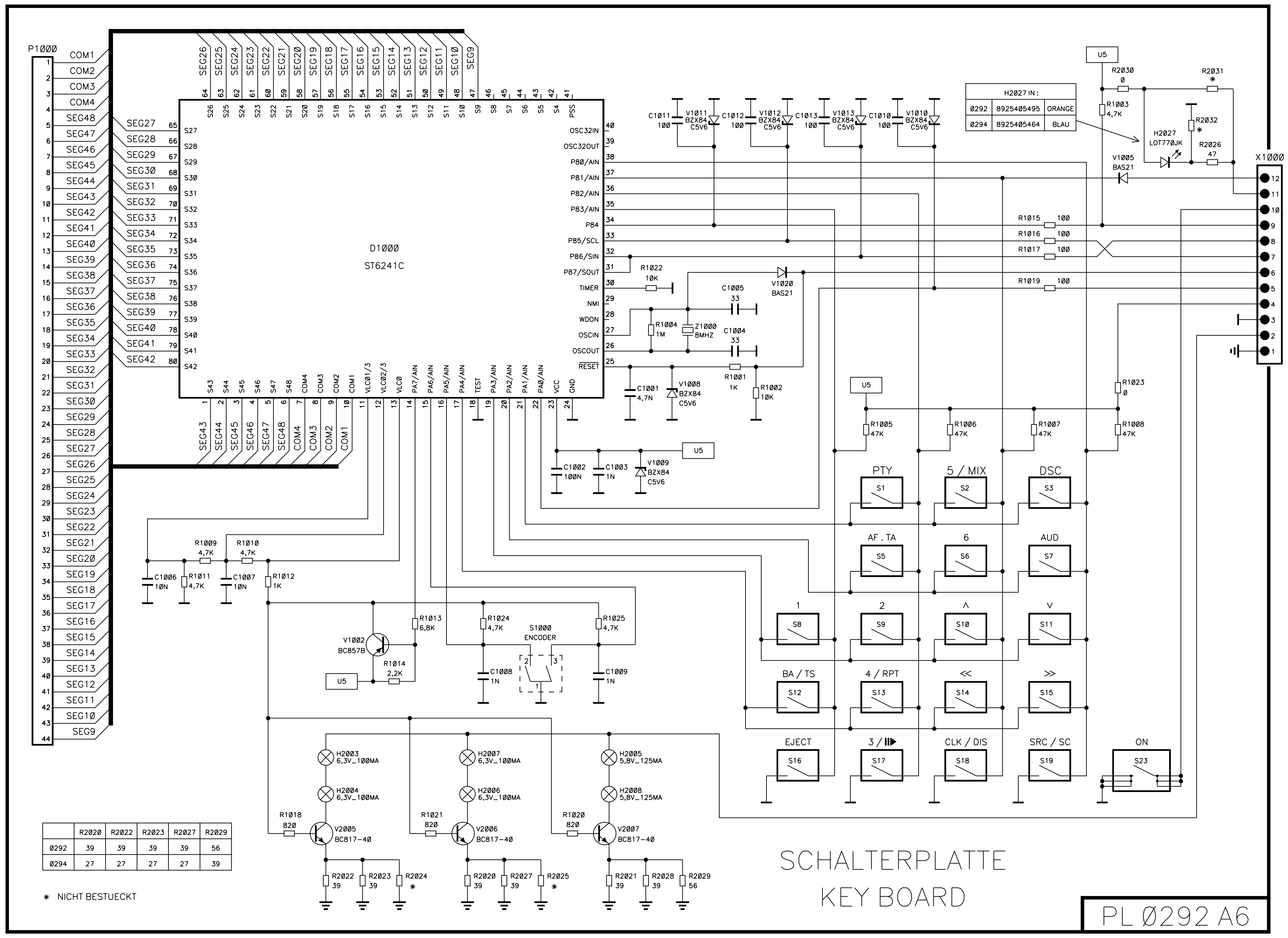
Schalterplatte
Key board
PL 0292 A06



Schalterplatte
Key board
PL 0292 A06
Chip

X1000	
1 = GND_ILLUMIN.	7 = PANEL_CLOCK
2 = ILLUMINATION	8 = PANEL_DATA
3 = GND_DIGITAL	9 = PANEL_DETECT/ SELECT
4 = U51	10 = ON_KEY
5 = PANEL_ ACK/REQ	11 = ON_LED
6 = PANEL_RESET	12 = CLK_REQUEST





H2027 IN :		
0292	8925405495	ORANGE
0294	8925405464	BLAU

	R2020	R2022	R2023	R2027	R2029
0292	39	39	39	39	56
0294	27	27	27	27	39

* NICHT BESTUECKT

BLAUPUNKT

AUTORADIO
Fun Line - CD / E

Sevilla RDM 168
7 648 554 310
7 648 554 312
7 648 554 315
7 648 554 318

Biarritz RDM 169
7 649 430 310

Acapulco RDM 168
7 648 558 310

Florida RD 168
7 648 551 310

San Remo RD 168
7 648 550 310

8 622 401 608 KR-WG 07/98 SB

Schaltbild • Circuit diagram

CLASS 1
LASER PRODUCT

UNSICHTBARE LASERSTRAHLUNG
NICHT DEM STRAHL AUSSETZEN
LASERKLASSE 3B

(D) VORSICHT!

Das Gerät beinhaltet eine Laserkomponente! Im Servicefall nachfolgende Hinweise beachten:

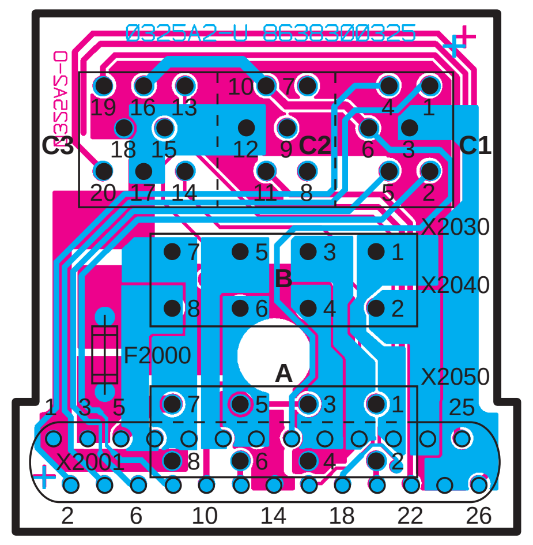
- Das Gerät arbeitet mit unsichtbarem Laserstrahl. Bei geöffnetem Gerät tritt im Bereich des Plattenfaches Laserstrahlung aus.
- Nicht in den Strahl blicken.
- Unbeteiligte Personen vom Arbeitsplatz fernhalten.
- Der Betrachtungsabstand darf 13cm nicht unterschreiten. Kann dies nicht eingehalten werden, muß eine geeignete Laserschutzbrille getragen werden.

(GB) CAUTION!

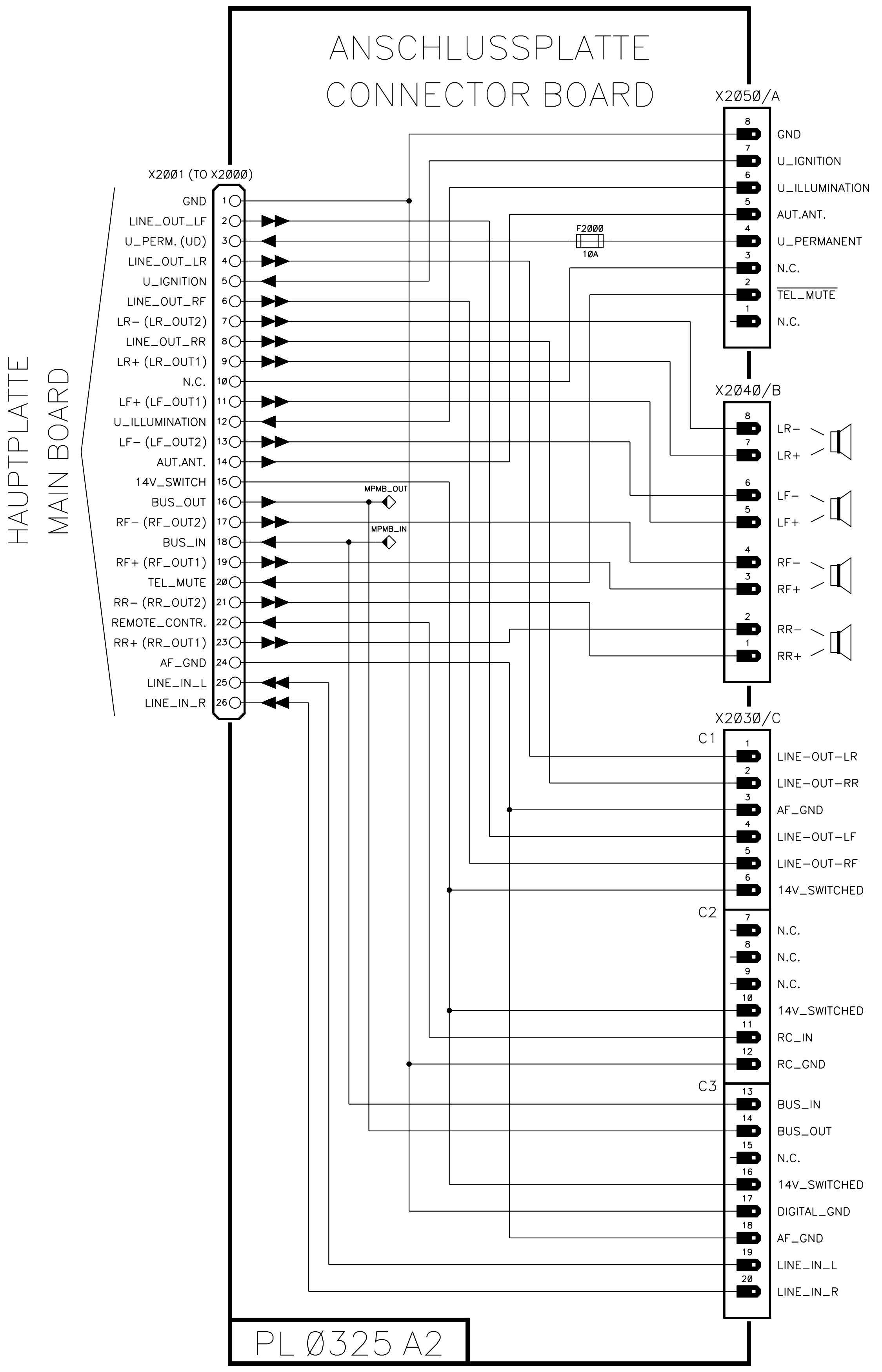
This unit contains a laser component! For service observe the following important instructions:

- The unit operates with an invisible laser beam. When the cover is removed, near the disc compartment, invisible laser beams are apparent.
- Avoid direct eye contact with these beams.
- Keep other people away from the working place.
- The viewing distance should not be less than 13cm. If this distance cannot be ensured, use suitable laser safety goggles.

Anschlußplatte
Connector board
PL 0325 A02



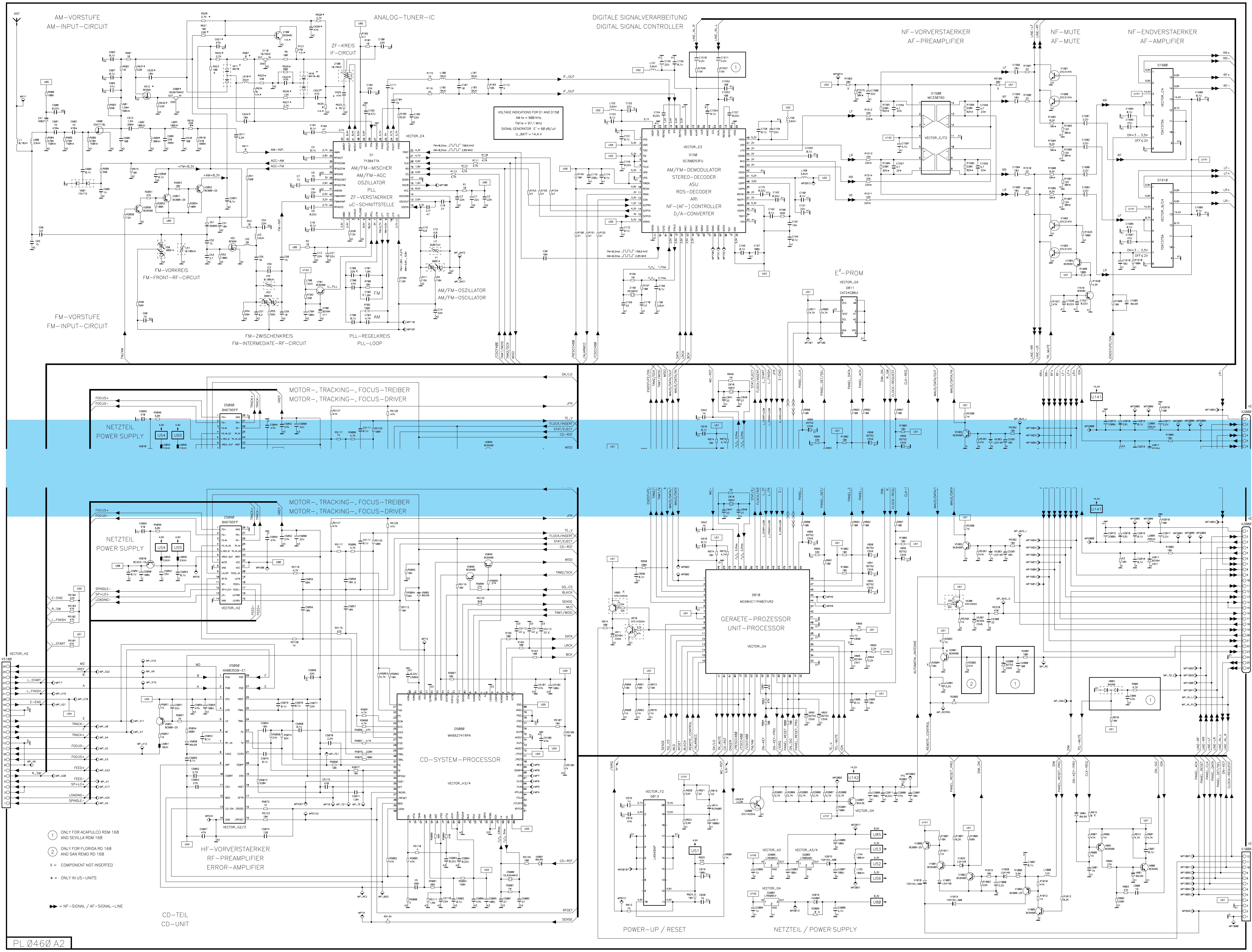
X2001 (TO X2000)	
1 = GND	14 = AUT.ANT.
2 = LINE_OUT_LF	15 = 14V_SWITCH
3 = U_PERM. (UD)	16 = BUS_OUT
4 = LINE_OUT_LR	17 = RF – (RF_OUT2)
5 = U_IGNITION	18 = BUS_IN
6 = LINE_OUT_RF	19 = RF + (RF_OUT1)
7 = LR – (LR_OUT2)	20 = TEL_MUTE
8 = LINE_OUT_RR	21 = RR – (RR_OUT2)
9 = LR + (LR_OUT1)	22 = REMOTE_CONT.
10 = N.C.	23 = RR + (RR_OUT1)
11 = LF + (LF_OUT1)	24 = AF_GND
12 = U_ILLUMINATION	25 = LINE_IN_L
13 = LF – (LF_OUT2)	26 = LINE_IN_R



Prüfdiagnose Tuner IC (D1) Diagnosis test tuner IC (D1)						
Pin	**Band**	**Frequenz**	**E'**	**Uss**	**Vermerke**	**Notice**
24+25 (ZF-OUT)	FM	97,1 MHz	83 dbµV	650 mVss	jeweils gegen Masse	respective against GND
28	FM	97,1 MHz	80 dbµV	25 mVss		
31+32	FM	97,1 MHz	80 dbµV	200 mVss	jeweils gegen Masse	respective against GND
31+32	AM	900 kHz	80 dbµV	200 mVss	jeweils gegen Masse	respective against GND
34 (AM-IN)	AM	900 kHz	80 dbµV	50 mVss		
36	AM	900 kHz	ab 73 dbµV		künstliche Antenne aus	not commutated
37	FM	97,1 MHz	ab 80 dbµV			
43 (FM-IN)	FM	97,1 MHz	94 dbµV	5 mVss		

Pin-Belegung des FM/AM Tuner-IC D1 Tuner IC D1 Pin configuration				
Pin No.	**I/O**	**Name**	**Funktion**	**Function**
1	-	MIXDEC	Mischer Entkopplung	Mixer decoupling
2	-	CINT	für PLL	for PLL
3	-	CHOLD	für PLL	for PLL
4	-	PLLGND	PLL - Masse	PLL Ground
5	-	VCC	8,5V	8,5V
6	-	VPLL	PLL Oberspannung	PLL top voltage
7	I	LFINP	Schleifenfiltereingang	PLL loop filter Input
8	O	LF1	Schleifenfilter 1	PLL loop filter Output 1
9	O	LF2	Schleifenfilter 2	PLL loop filter Output 2
10	O	LF3	Schleifenfilter 3	PLL loop filter Output 3
11	I	VTUNE	Abstimmspannung	Tuning voltage
12	I	OSCINP	Oszillator Eingang	Oscillator Input
13	O	OSCOUT	Oszillator Ausgang	Oscillator Output
14	-	OSCGND	Oszillator Masse	Oscillator Ground
15	O	VCC	8,5V	8,5V
16	O	OSCBUF	Oszillatorausgangstreiber	Oscillator Buffer Output
17	I	DGND	Digitale Masse	Digital Ground
18	I	CS	Chip Select	Chip Select
19	I	RD	Dateneingang	DATA IN
20	I	CLK	Clock	Clock
21	O	TX	Datenausgang	DATA OUT
22	I	FREF	Referenzfrequenz	Reference frequency
23	-	IFAGC2	ZF Regelspannung 2	IF AGC 2
24	O	IFOUT1	ZF - Ausgang 1	IF output 1
25	O	IFOUT2	ZF - Ausgang 2	IF output 2
26	-	IFAGC1	ZF Regelspannung 1	IF AGC 1
27	-	IFGND	ZF Masse	IF Ground
28	I	IFIN	ZF Eingang	IF Input
29	-	VDC	Interne Referenzspannung	Internal reference voltage
30	-	VCC	8,5V	8,5V
31	O	MIXOUT2	Mischerausgang 2	Mixer Output 2
32	O	MIXOUT1	Mischerausgang 1	Mixer Output 1
33	-	AMREF	AM - Referenzeingang	AM reference Input
34	I	AMMIXIN	AM Mischereingang	AM Mixer Input
35	-	RFAGC3	HF Regelzeitkonstante (aufregeln)	RF AGC 3
36	O	RFAGCAM	HF Steuerspannung Vorstufe AM	RF AGC for AM input stage
37	O	RFAGCFM	HF Steuerspannung Vorstufe FM	RF AGC for FM input stage
38	-	MIXGND	Mischer Masse	Mixer Ground
39	-	RFAGC2	HF Regelzeitkonstante (Detektor)	RF AGC 2
40	-	RFAGC1	HF Regelzeitkonstante (abregeln)	RF AGC 1
41	-	ANGGND	Analog Masse	Analog ground
42	-	FMMIXREF	Referenzspannung FM Mischer	Reference voltage FM mixer
43	I	FMMIXINP	FM Mischer Eingang	FM mixer input
44	-	RFAGCD	AGC Entkopplung	AGC decoupling

Pin-Belegung des IC D150 Digital IC D150 Pin Configuration				
Pin No.	**I/O**	**Name**	**Funktion**	**Function**
1	-	VSS	Masse	Ground
2	-	VDD	5 V	5 V
4	I	TDI	Testdateneingang	Test Data Input
5	I	TMS	Test Mode	Testmode
6	I	TCKL	Test Clock	Testclock
7	-	VDD	5 V	5 V
8	-	VSS	Masse	Ground
9	-	PWDN	Power down Zustand	Power down Mode
10	I	RESN	Reset	Hardware reset (active LOW)
11	O	IRQN	RDS Alarm/SLS	RDS alarm/search stop
12	I	CSN	Chip select Eingang	Chip select µC interface
13	I	SCPRX	Serielle Daten µC Interface	Serial data µC interface IN
14	O	SCPTX	Serielle Daten µC Interface	Serial data µC interface OUT
15	I	SCPCK	Clock µC Interface	Clock µC interface
16	-	VDDIO	Plusspannung Digitale Ein-/Ausgänge	Voltage for digital I/O
17	-	VSSIO	Masse Digitale Ein-/Ausgänge	Ground for digital I/O
18	O	CKL1	Programmierbarer Clock 1	Programmable clock 1
20	I	XTALI	28,5 MHz Oszillator	Oscillator 28,5 MHz
21	O	XTALO	28,5 MHz Oszillator	Oscillator 28,5 MHz
31	I	TDI1	Testdateneingang 1	Test Input 1
32	-	VDD	5 V	5 V
33	-	VSS	Masse	Ground
35	-	VSSPLL	Masse (Minus) PLL	Ground (minus) PLL
36	-	VDDPLL	Plus PLL 5V	PLL 5V (pos.)
37	O	REFP1	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
38	O	REFN1	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
39	-	CAPN	PLL Kapazität (negativ)	PLL capacity (neg.)
40	-	CAPP	PLL Kapazität (positiv)	PLL capacity (pos.)
41	-	VDDO	Audio D/A - Wandler 5V	Audio D/A converter (+5V)
42	-	VSSO	Audio D/A - Wandler Masse	Audio D/A converter (ground)
44	O	RFO	Audio Rechts (analog)	Analogic audio right
45	-	OGND	Masse Analogausgänge	Ground
46	-	LFO	Audio Links (analog)	Analogic audio left
48	-	VDDA	5V A/D - Wandler	5V A/D - converter
49	-	VSSA	Masse A/D - Wandler	Ground A/D - converter
50	O	REFP3	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
51	O	REFN3	Audio D/A-WandlerNegative Referenz	Audio D/A converter (neg. reference)
52	I	AUXL	Externer Eingang links	Auxillary left
53	I	CCL	Cassette Eingang links	Cassette input left
54	-	AGND	Audioeingänge Masse	Ground for Audio inputs
55	I	CCR	Cassette Eingang rechts	Cassette input right
56	I	AUXR	Externer Eingang rechts	Auxillary left right
57	-	VDDR	5 V	5 V
58	-	VSSR	Masse	Ground
59	O	REFP2	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
60	I	IFP	ZF Eingang (plus)	Positif IF input
61	I	IFN	ZF Eingang (minus)	IF input (neg.)
62	O	REFN2	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
63	-	VSSIF	ZF A/D - Wandler (minus)	IF A/D converter (-)
64	-	VDDIF	ZF A/D - Wandler 5 V	IF A/D converter (+5V)



① ONLY FOR ACAPULCO RDM 168 AND SEVILLA RDM 168

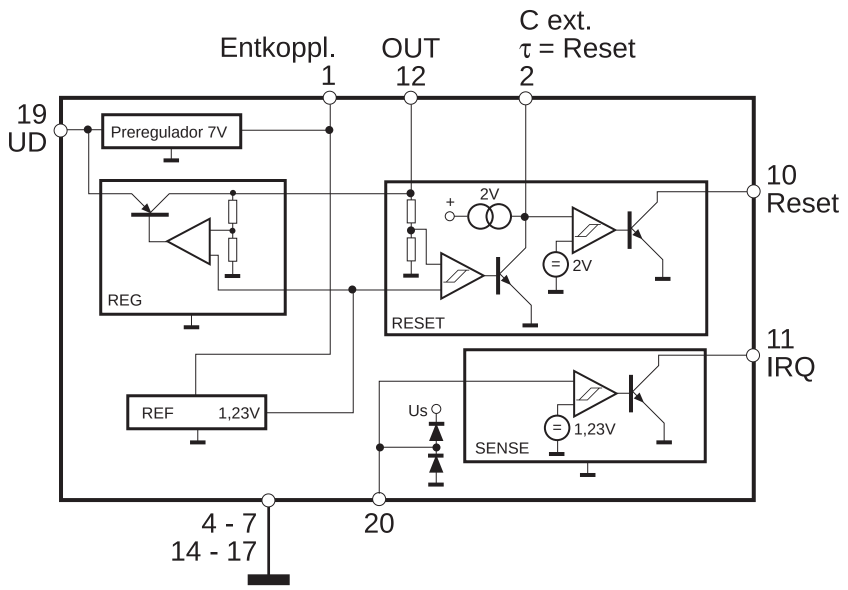
② ONLY FOR FLORIDA RD 168 AND SAN REMO RD 168

X = COMPONENT NOT INSERTED

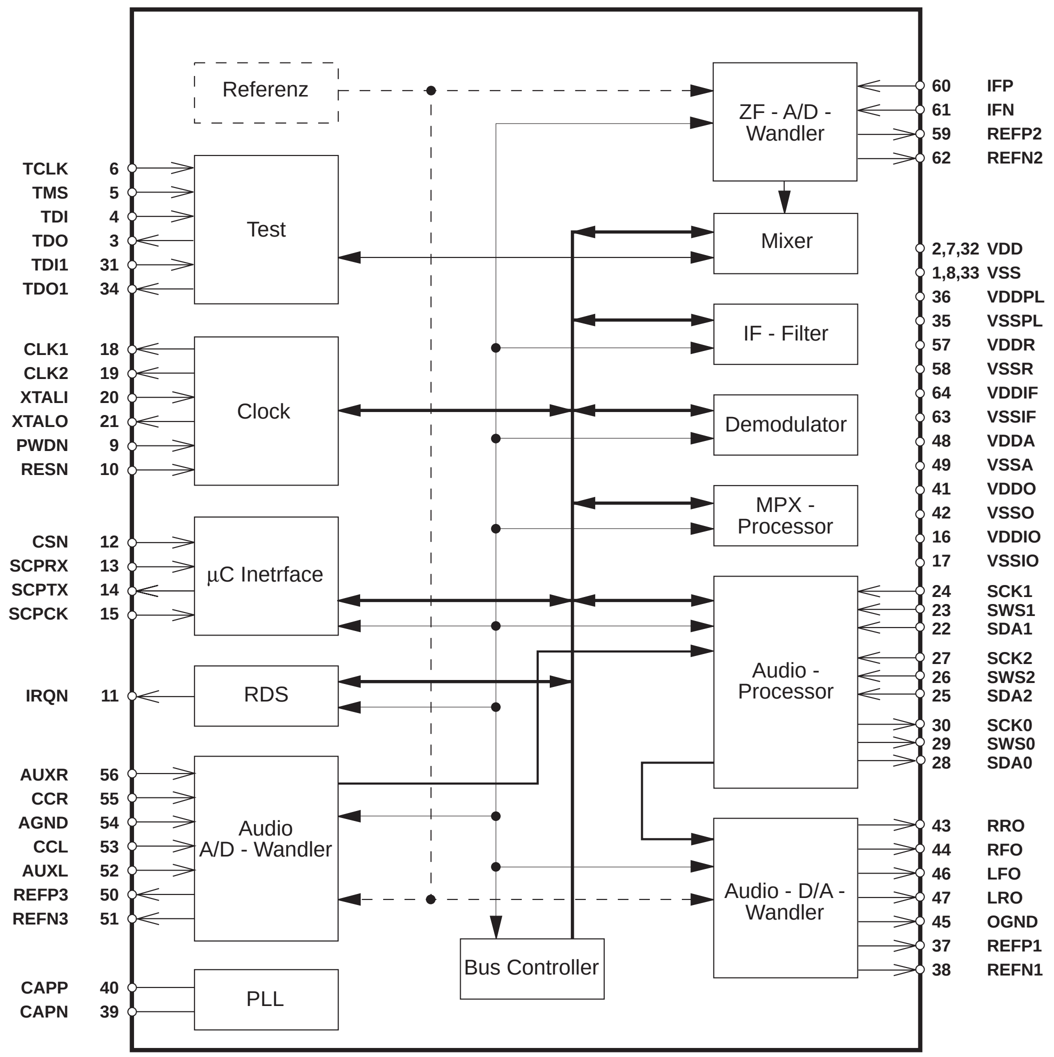
✱ = ONLY IN US–UNITS

▶▶ = NF–SIGNAL / AF–SIGNAL–LINE

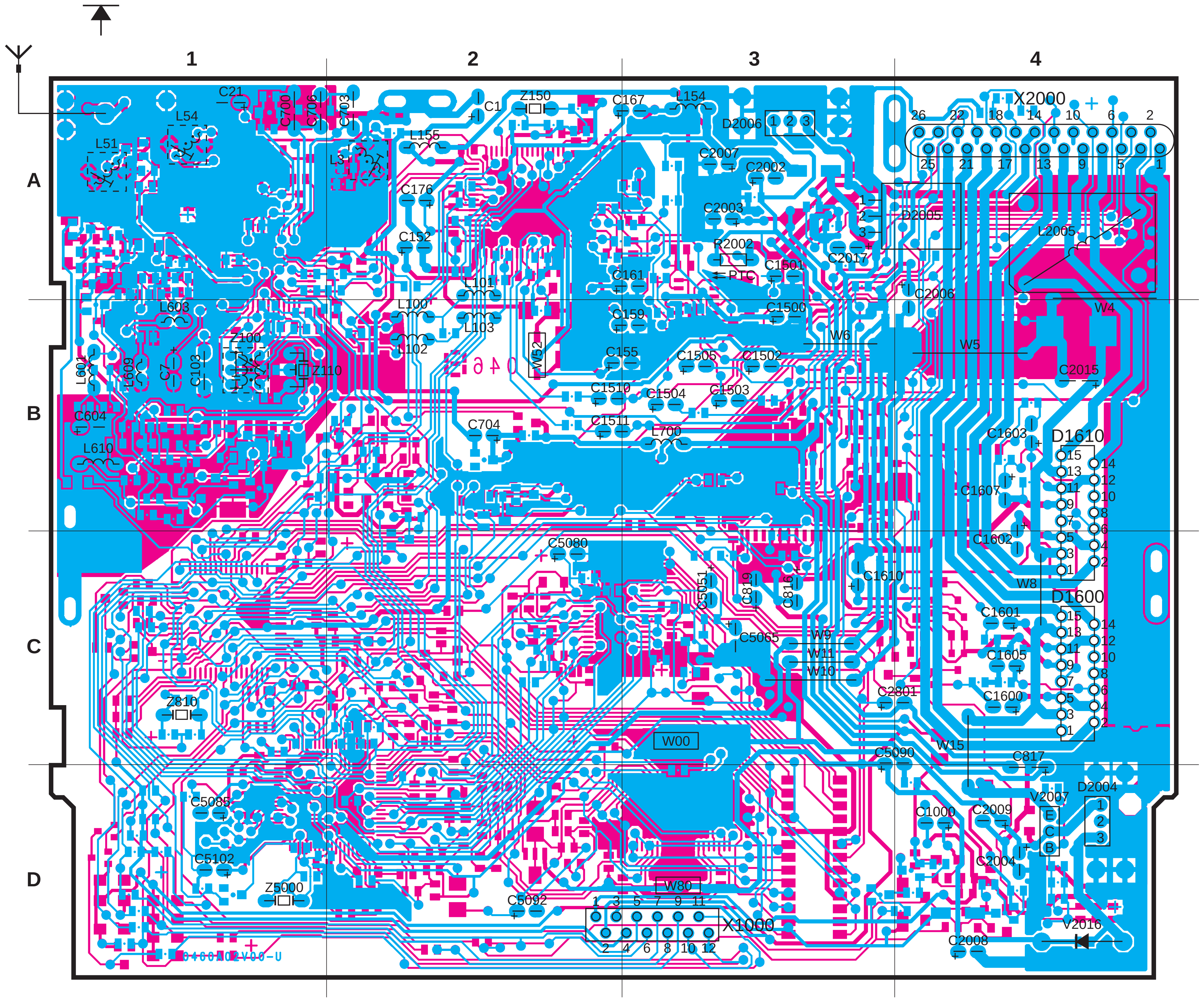
L4949 / D813



D150



Hauptplatte
Main board
PL 0460 A02



X2000
(TO X2001)

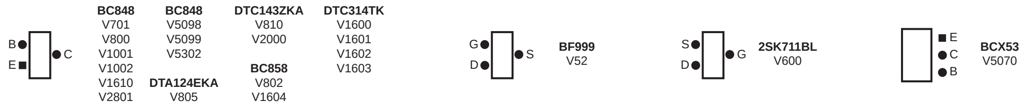
1 = GND
2 = LINE_OUT_LF
3 = U_PERM. (UD)
4 = LINE_OUT_LR
5 = U_IGNITION
6 = LINE_OUT_RF
7 = LR – (LR_OUT2)
8 = LINE_OUT_RR
9 = LR + (LR_OUT1)
10 = N.C.
11 = LF + (LF_OUT1)
12 = U_ILLUMINATION
13 = LF – (LF_OUT2)
14 = AUT.ANT.
15 = 14V_SWITCH
16 = BUS_OUT
17 = RF – (RF_OUT2)
18 = BUS_IN
19 = RF + (RF_OUT1)
20 = TEL_MUTE
21 = RR – (RR_OUT2)
22 = REMOTE_CONT.
23 = RR + (RR_OUT1)
24 = AF_GND
25 = LINE_IN_L
26 = LINE_IN_R

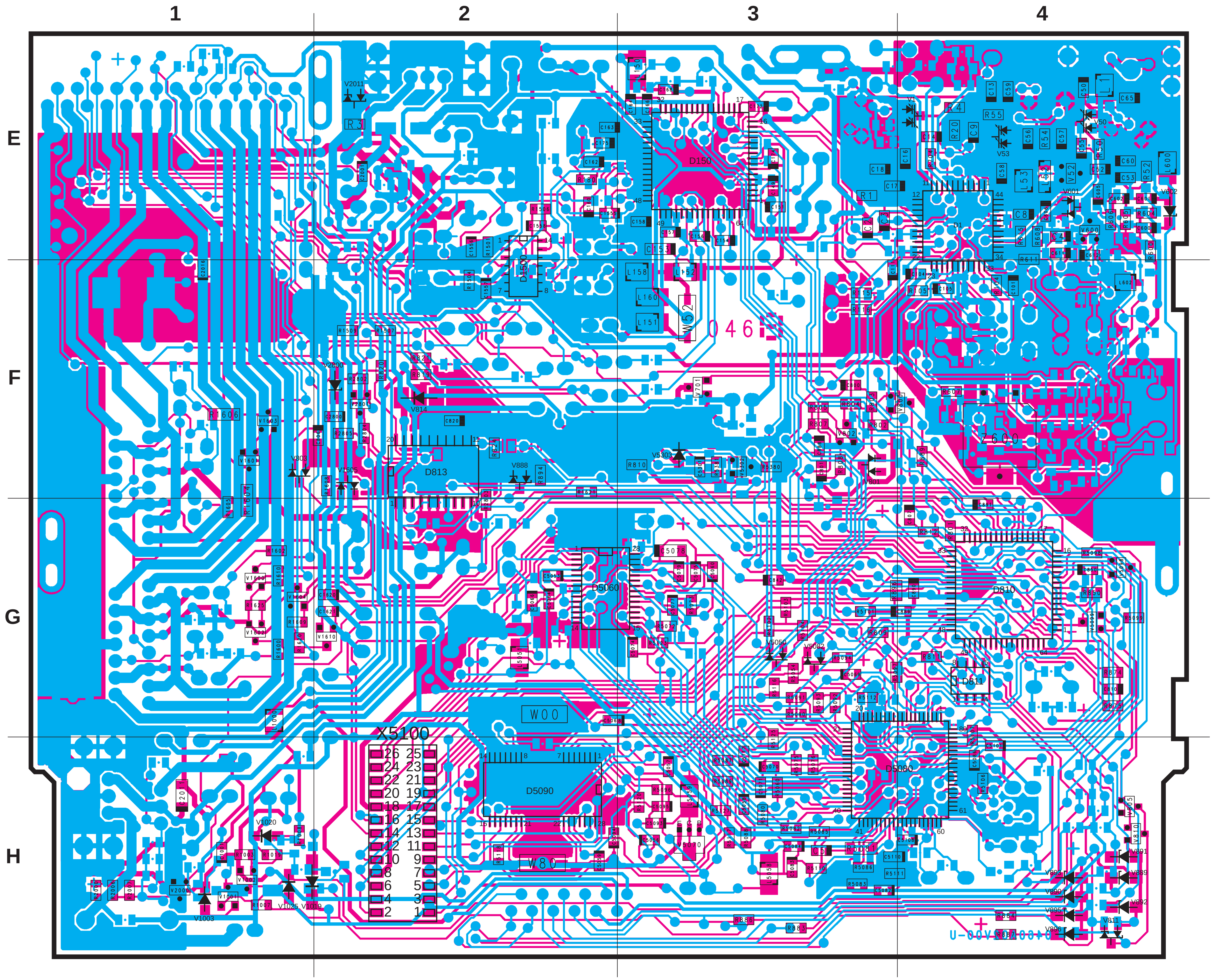
X1000

1 = GND_ILLUMIN.
2 = ILLUMINATION
3 = GND_DIGITAL
4 = U51
5 = PANEL_ACK/REQ
6 = PANEL_RESET
7 = PANEL_CLOCK
8 = PANEL_DATA
9 = PANEL_DETECT/
SELECT
10 = ON_KEY
11 = ON_LED
12 = CLK_REQUEST

Hauptplatte
Main board
PL 0460 A02

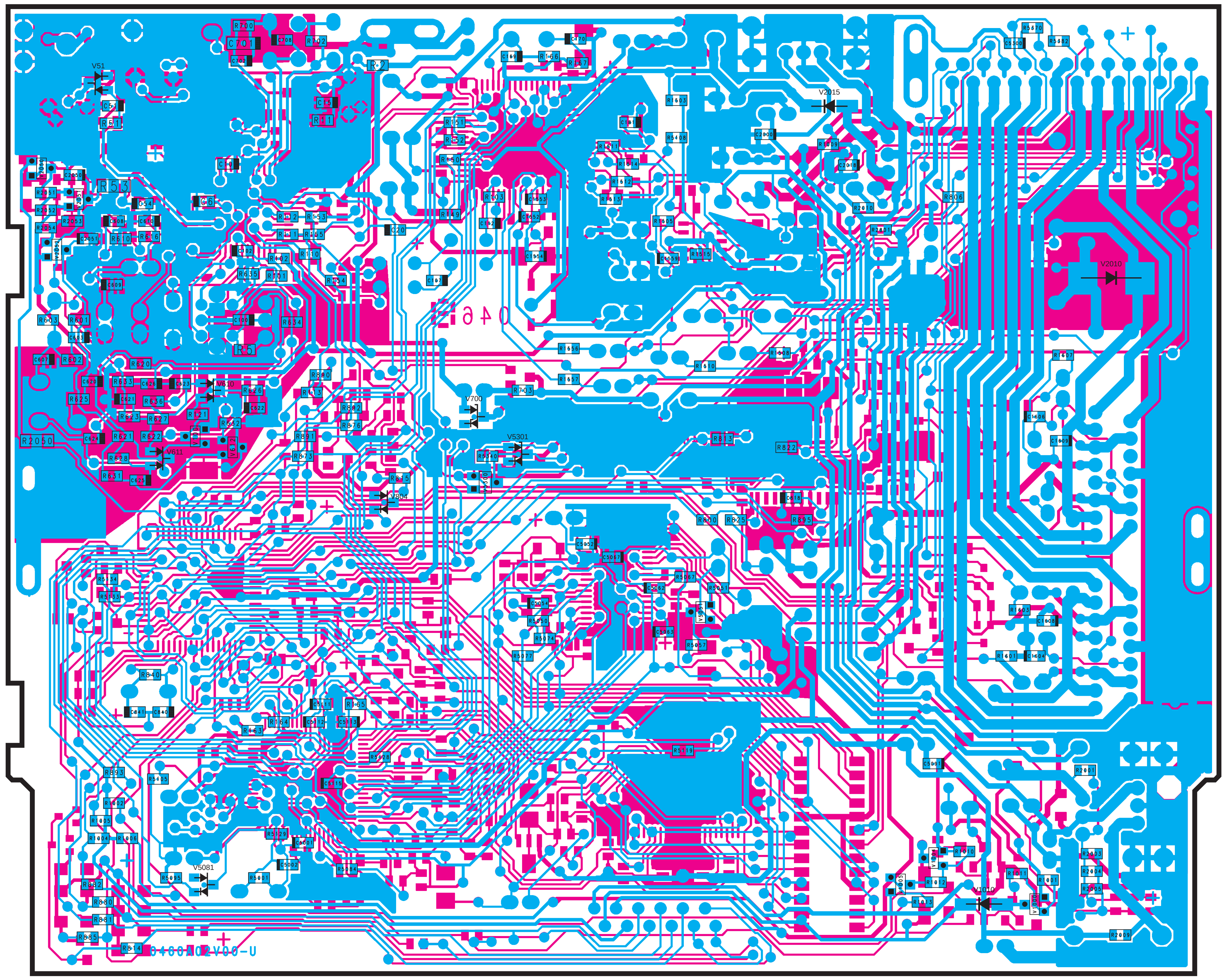
Chip





Hauptplatte
Main board
PL 0460 A02

Chip



X5100

1 = SPMOT –
2 = LDMOT –
3 = SPMOT +
4 = LDMOT +
5 = FDMOT –
6 = REST_SWITCH
7 = FDMOT +
8 = GND3
9 = FA +
10 = LD
11 = FA –
12 = GND1
13 = TA +
14 = F
15 = TA –
16 = E
17 = GND2
18 = LDGND
19 = END (SW2)
20 = U54
21 = FINISH (SW3)
22 = A
23 = START (SW1)
24 = B
25 = VREF
26 = LPD